AMENDMENTS TO THE SPECIFICATION

Replace the paragraph beginning on page 7, line 4, with the following:

The sub-chip offset data streams generated by the sub-chip delay circuits 205a-205k are each provided to at least one despreader circuit 215. Particularly, as illustrated in Figure 2A, the sub-chip offset data streams (except the data stream with an offset of one chip) are each provided to two despreader circuits 215. Each of the despreader ~~circuit~~ circuits 215 then despreads a data stream with a locally generated code sequence (received via bus 205) to produce despread data. Despreaders are well known in the art. A description of an exemplary despreader circuit may be found in co-pending U.S. patent application serial no. ~~(TBD) (attorney docket number 009824-0036-999)~~ 09/751,785, which is entitled "A Configurable Multi-mode Despreader for Spread Spectrum Applications," and which is assigned to the present assignee.